Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2914

Chopper Regulator, DC-DC Converter and Moter Drive Applications

• Low drain-source ON resistance : RDS (ON) = 0.42Ω (typ.)

• High forward transfer admittance $: |Y_{fs}| = 7.5 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 250 \text{ V)}$

• Enhancement mode : $V_{th} = 1.5 \text{ to } 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	250	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V_{DGR}	250	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	7.5	А	
	Pulse (Note 1)	I _{DP}	30	A	
Drain power dissipatio	n (Tc = 25°C)	PD	20	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	110	mJ	
Avalanche current		I _{AR}	7.5	Α	
Repetitive avalanche	energy (Note 3)	E _{AR}	2	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature r	ange	T _{stg}	-55 to 150	°C	

Weight: 2.0 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 3.3 mH, R_G = 25 Ω , I_{AR} = 7.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

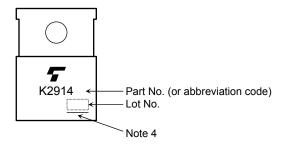
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μA
Drain cut-off cur	rrent	I _{DSS}	V _{DS} = 250 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	250	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3.5 A	_	0.42	0.5	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3.5 A	4	7.5	_	S
Input capacitano	е	C _{iss}			700	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		80	_	pF
Output capacitar	utput capacitance C _{oss}			270	_		
Switching time -	Rise time	t _r	$V_{\rm GS}$ 0 0 0 0 0 0 0 0 0 0	_	10	_	
	Turn-on time	t _{on}		_	20	_	ns
	Fall time	t _f		_	10	_	115
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$	_	70	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	20	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$		13		nC
Gate-drain ("miller") Charge		Q_{gd}			7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	7.5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 7.5 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 7.5 A, V _{GS} = 0 V		180	_	ns
Reverse recovery charge	Q_{rr}	dI _{DR} / dt = 100 Å / μs		1.1	_	μC

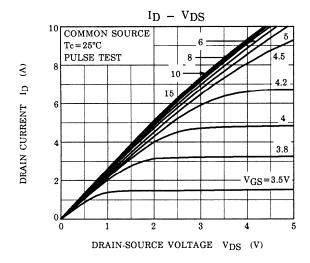
Marking

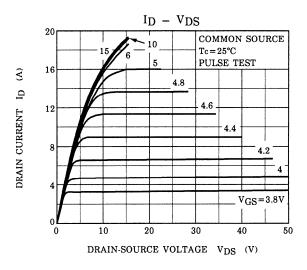


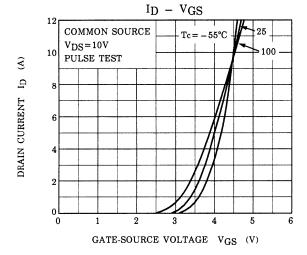
Note 4: A line under a Lot No. identifies the indication of product Labels.

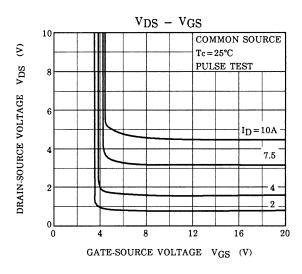
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

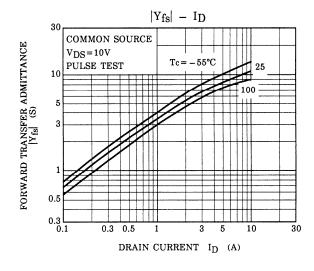
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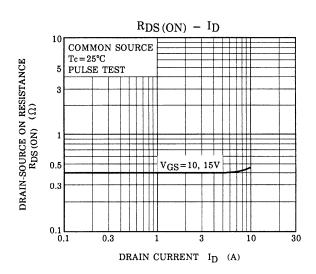




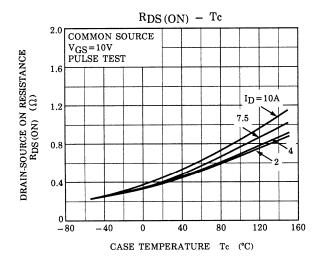


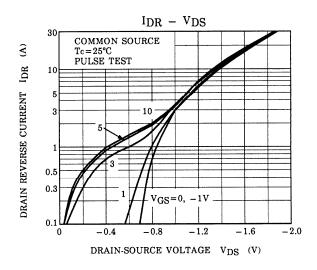


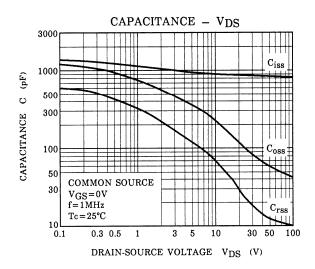


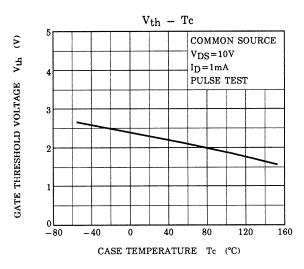


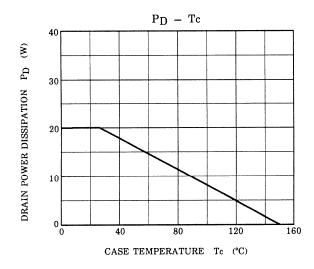
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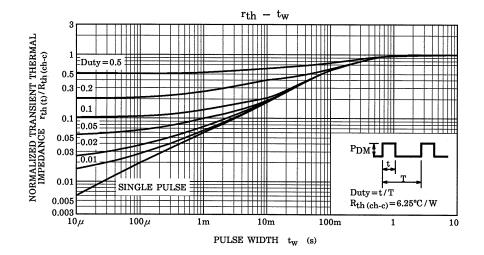


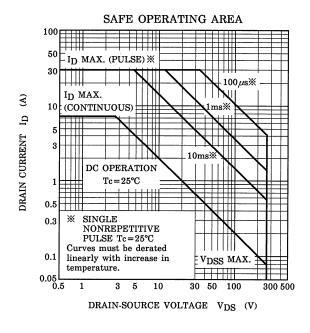


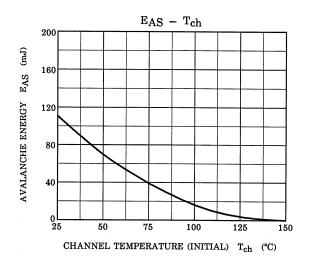


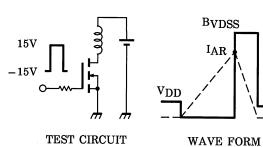












$$R_G$$
 = 25 Ω
 V_{DD} = 50 V, L = 3.3 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

VDS

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